

said probe tip ends" is for example shown as 112 on Figure 13 described in the first full paragraph at page 15. The Examiner states "In claim 22-23, it is unclear what "an electrical interconnection means" and "a means for disposing said probe tip ends in electrical contact with contact location on said electronic device" represent". The first quoted passage, for example, corresponds to electrically conductors within substrate 68 of Figure 2, an example specific embodiment of which is described at page 8, lines 6-7, as "a standard printed circuit board", which known to persons of skill in the art have embedded electrical conductors providing "an electrical interconnection means" from one side to the other of the printed circuit board. Antecedent basis has been provided for "said probe tip ends". The second quoted passage is shown schematically in Figures 12 and 18.

Claims 22-23 have been rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Applicants respectfully disagree and request withdrawal of this rejection.

For the reasons given above Applicants disagree with the Examiner's statement that "It appears that the specification does not have support for the limitations of an electrical interconnection means" and "a means for disposing said probe tip ends in electrical contact with contact location on said electronic device" as recited in claims 22-23."

A Terminal Disclaimer is attached to overcome the double patenting rejection.

In view of the changes to the claims and the remarks herein, the Examiner is respectfully requested to reconsider the above-identified application. If the Examiner wishes to discuss the application further, or if additional information would be required, the undersigned will cooperate fully to assist in the prosecution of this application.

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S.N. 09/088,394

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